ECE 65 – Components and Circuits Lab

Lab 1 Report – Circuit Simulations

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# Abstract

The purpose of this lab is to \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_. We performed \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_, and \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_. We concluded that \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_.

# Experimental Procedures and Results

## Problem 1: Voltage Divider (Bias Point, DC Sweep, Parametric sweep, plotting)

Diagram, schematic

Description automatically generated

Figure 1: Circuit of Voltage Divider

*Include your explanations, circuit analysis, plots, etc. here.*

## Problem 2: RC Circuit (VPULSE function, time-domain analysis, plotting)

A picture containing antenna

Description automatically generated

Figure 2: Circuit of RC Circuit

*Include your explanations, circuit analysis, plots, etc. here.*

## Problem 3: Op-amp as a Voltage Amplifier (Sine function, time-domain analysis, plotting)

Chart, diagram, schematic

Description automatically generated

Figure 3: Circuit of Voltage Amplifier

*Include your explanations, circuit analysis, plots, etc. here.*

# Conclusion

*Include your conclusion here.*